



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Teruyuki Maeda

U.S.S.N.:

10/735,399

EXAMINER: Not yet assigned

FILED:

December 11, 2003

GROUP:

2811

FOR:

POWER TRANSISTOR AND SEMICONDUCTOR INTEGRATED CIRCUIT

**USING THE SAME** 

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

## **CERTIFICATION UNDER 37 C.F.R. 1.10\***

(Express Mail label number is mandatory.)
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I hereby certify that this correspondence and the documents referred to as attached therein are being deposited with the United States Postal Service on this date <u>April 12, 2004</u> in an envelope as "Express Mail Post Office to Addressee," mailing Label Number <u>EV 438 971 726 US</u> addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

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Applicant requests that all further communications from the Patent and Trademark Office in connection with the subject application be sent to the following address:

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